

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/615,938
Applicant : Kenji Shimazaki et al.
Filed : July 13, 2000
Title : METHOD OF ANALYZING ELECTROMAGNETIC
INTERFERENCE

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Conf. No. : 3985
TC/A.U. : 2857
Examiner : Mary C. Baran

AUG 19 2004

OFFICE OF PETITIONS

Customer No. : 000,116
Docket No. : 32809

INFORMATION DISCLOSURE STATEMENT

Mail Stop Petition
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

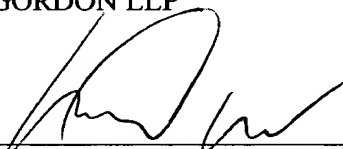
In accordance with 37 C.F.R. § 1.98, applicant is submitting herewith Form PTO-1449 listing references for consideration by the Examiner. Also submitted herewith is a legible copy of each reference listed.

If any further fees are required by this communication, please charge such fees to our Deposit Account No. 16-0820, Order No. 32809.

Respectfully submitted,

PEARNE & GORDON LLP

By


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Date: August 18, 2004

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 32809	SERIAL NO. 09/615,938
INFORMATION DISCLOSURE CITATION BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)		APPLICANT: Kenji Shimazaki et al.	
		FILING DATE: July 13, 2000	GROUP ART UNIT: 2857
Page 1 of 1			

U.S. PATENT DOCUMENTS							
Examiner Initial		Document No.	Date	Name	Class	Subclass	Filing Date
	A						
	B						
	C						
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FOREIGN PATENT DOCUMENTS							
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	M						
	N						
	P						
	Q						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
	R	Synopsis: Prime Power, Full-Chip Dynamic Power Analysis for Multimillion-Gate Designs, 2004.					
	S						
	T						

Examiner:	Date Considered
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*Examiner: Initial if reference considered, regardless of whether citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.